

09/53 8753

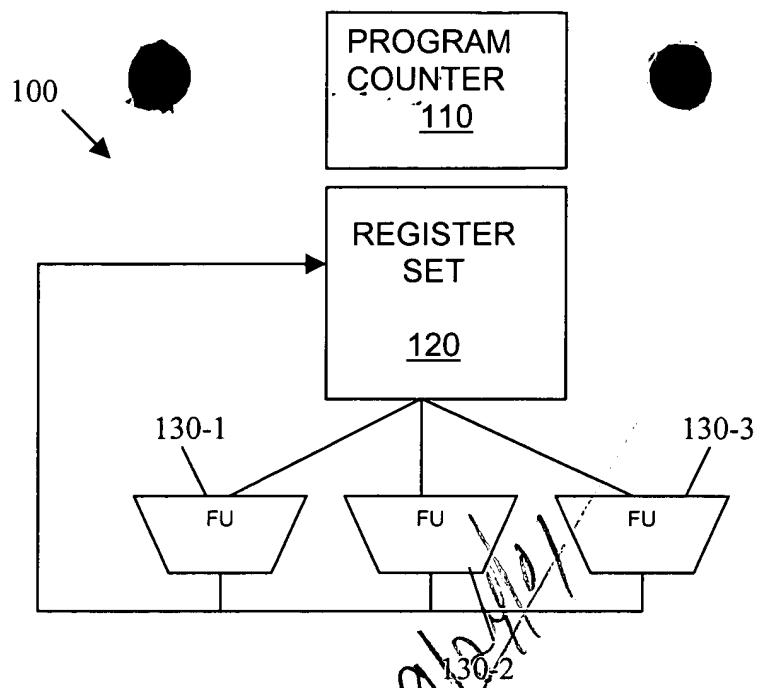


FIG. 1

PRIOR ART

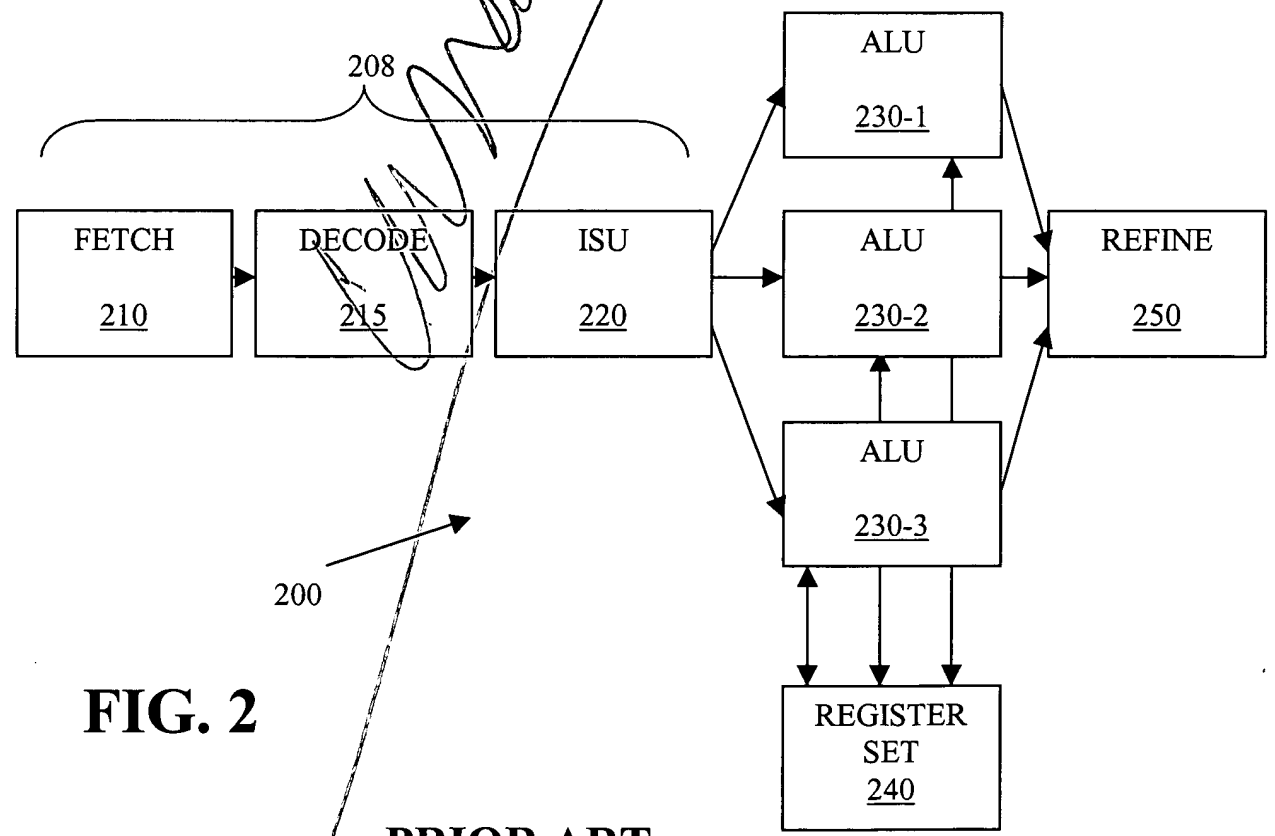


FIG. 2

PRIOR ART

7102

310 { L3: ADD R0, R1, R2
L2: SUB R3, R4, R2
L1: OR R6, R1, R5

FIG. 3

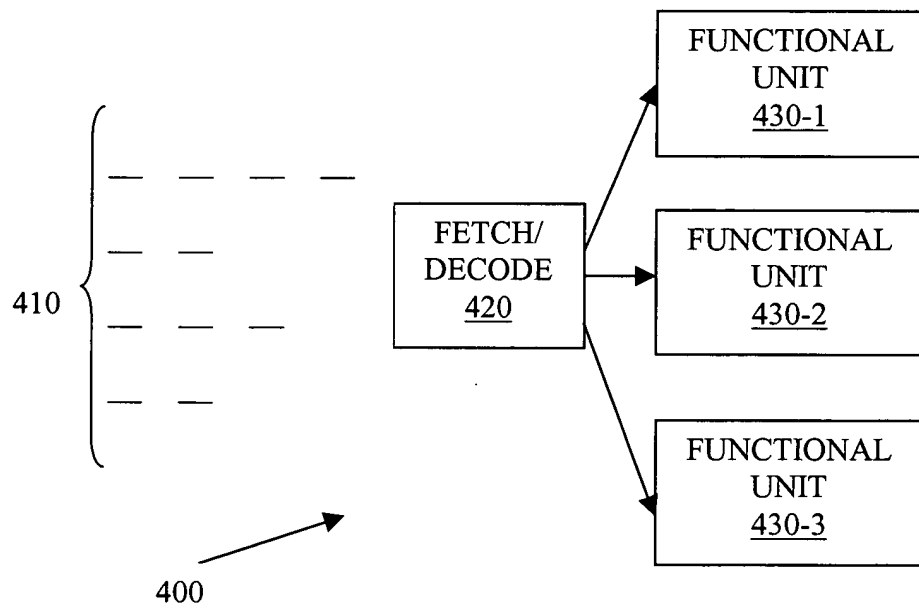


FIG. 4

PRIOR ART

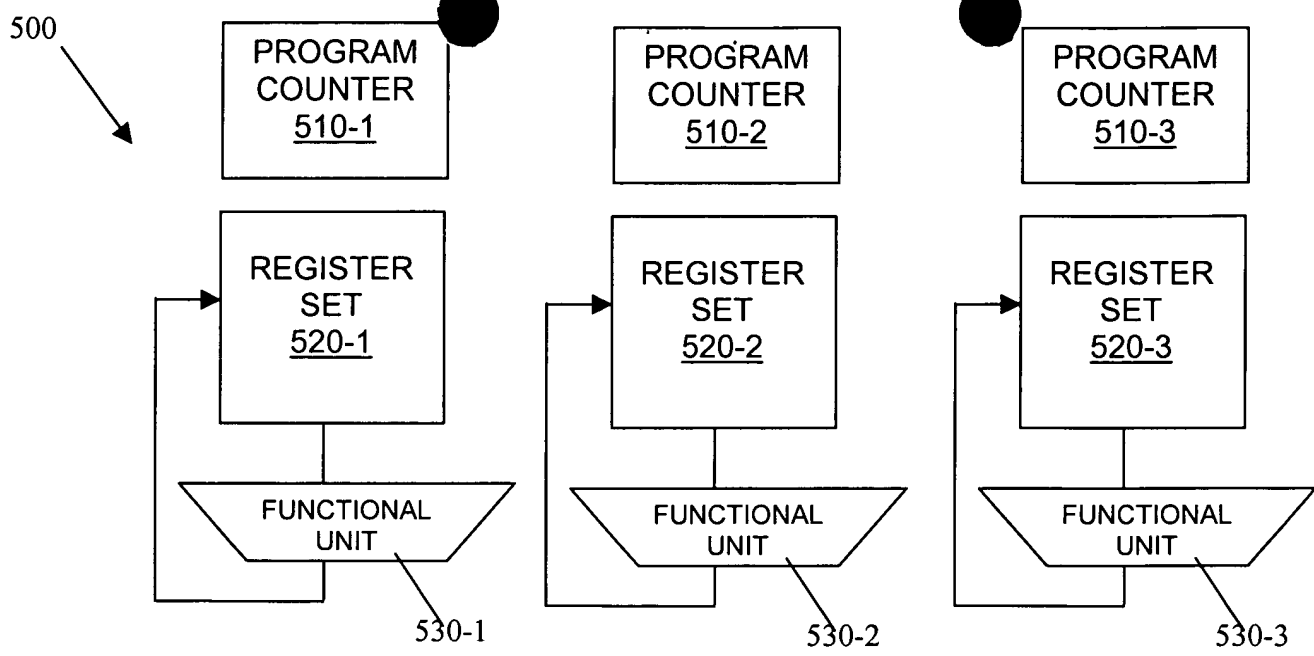


FIG. 5 **PRIOR ART**

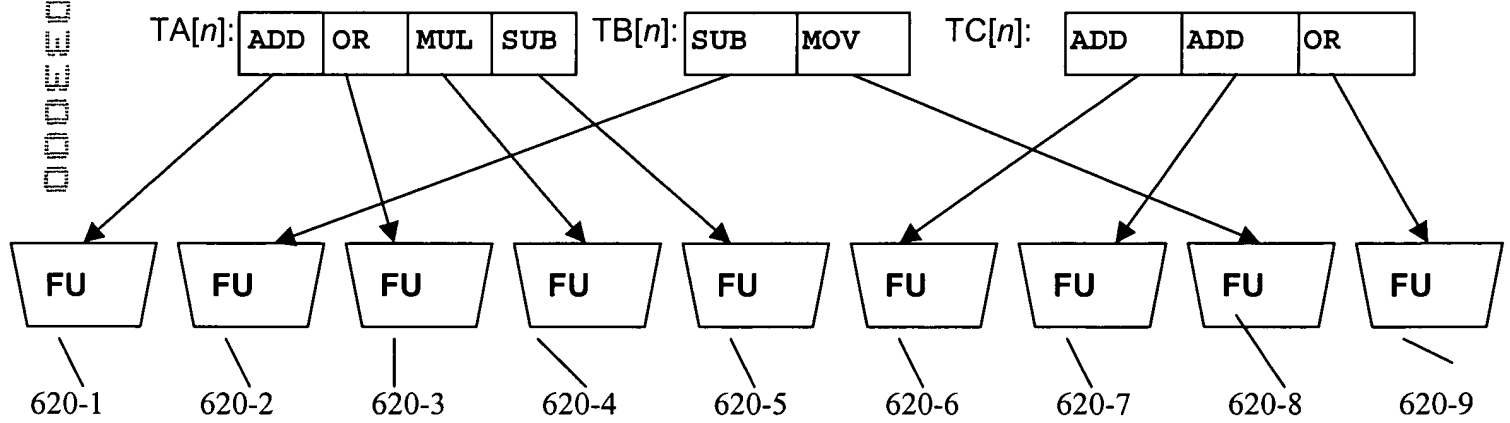


FIG. 6

700

FETCH GET A PACKET (UP TO K INSTRUCTIONS) FROM MEMORY <u>710</u>	DECODE DETERMINE FUs AND REGISTERS FOR UP TO K INSTRUCTIONS <u>720</u>	EXECUTE: PERFORM OPERATION AND PROCESS RESULT <u>730</u>
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FIG. 7A

PRIOR ART

750

FETCH GET UP TO N PACKETS (UP TO K INSTRUCTIONS) FROM MEMORY <u>760</u>	DECODE DETERMINE FUs AND REGISTERS NEEDED FOR UP TO N*K INSTRUCTIONS <u>770</u>	ALLOCATE SELECT APPROPRIATE INSTRUCTIONS AND ASSIGN THEM TO FUs <u>780</u>	EXECUTE: PERFORM OPERATION AND PROCESS RESULT <u>790</u>
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FIG. 7B

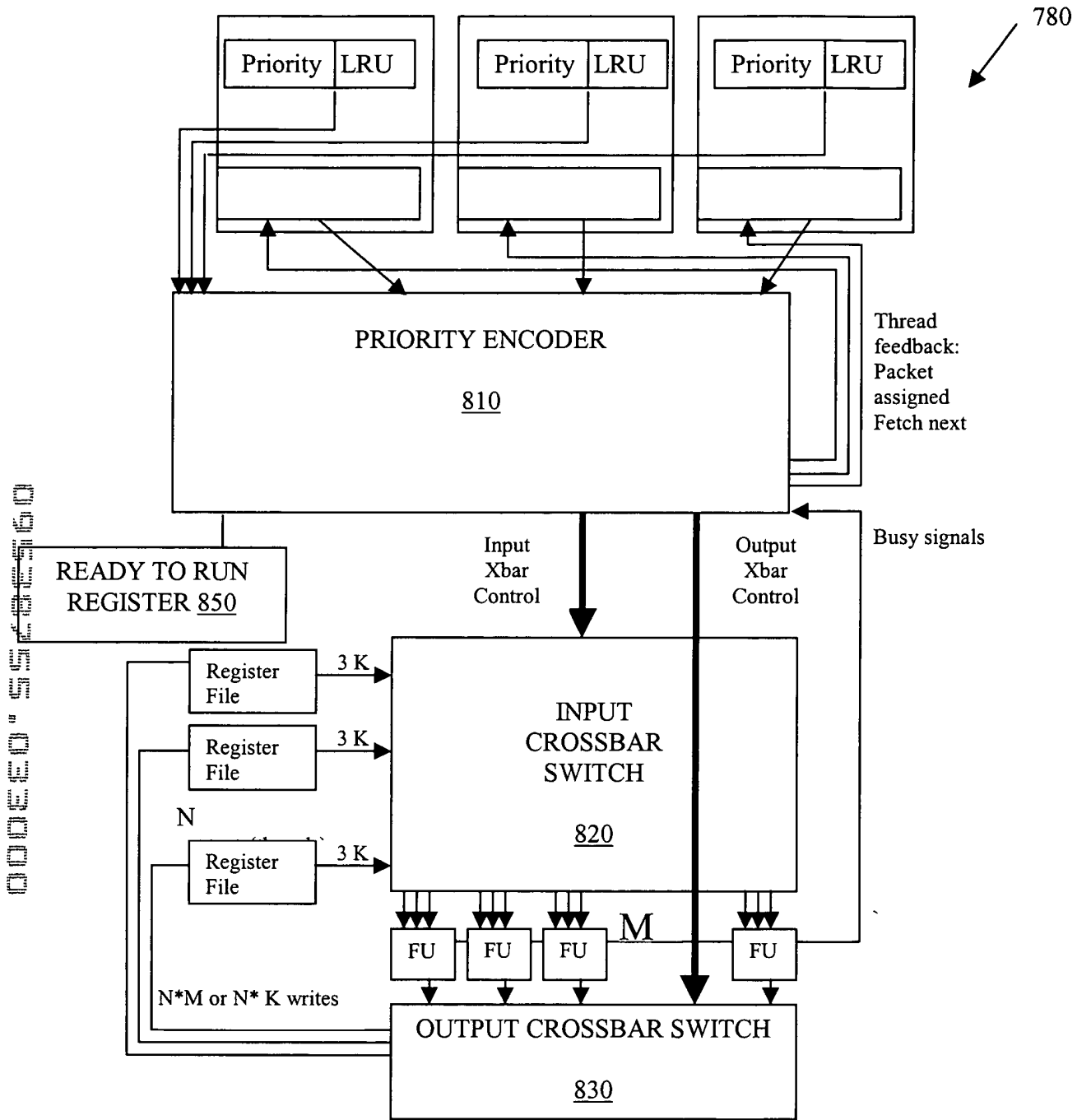


FIG. 8

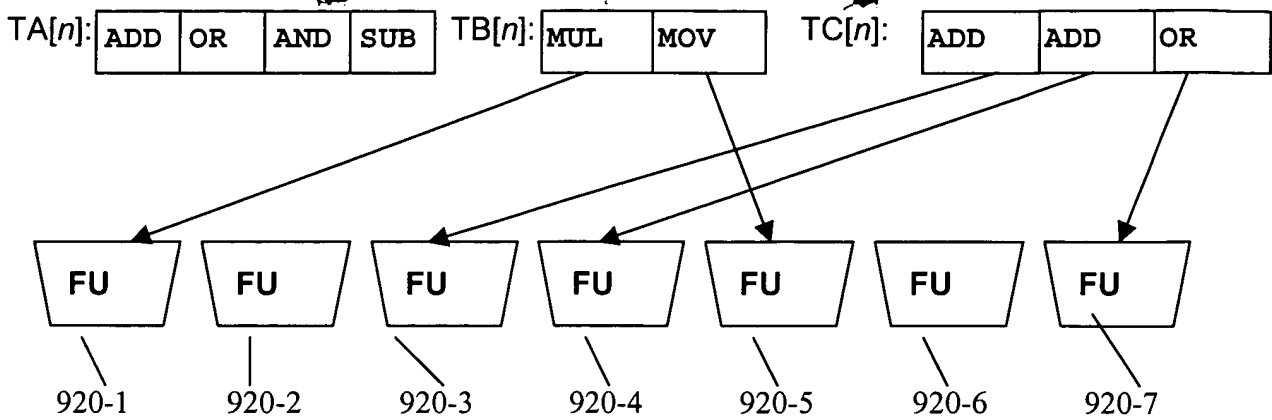


FIG. 9

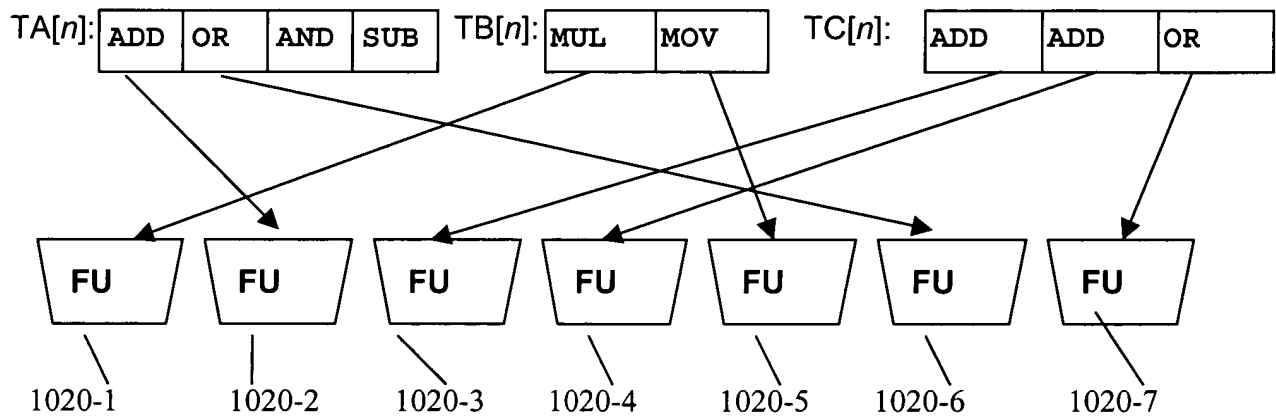


FIG. 10A

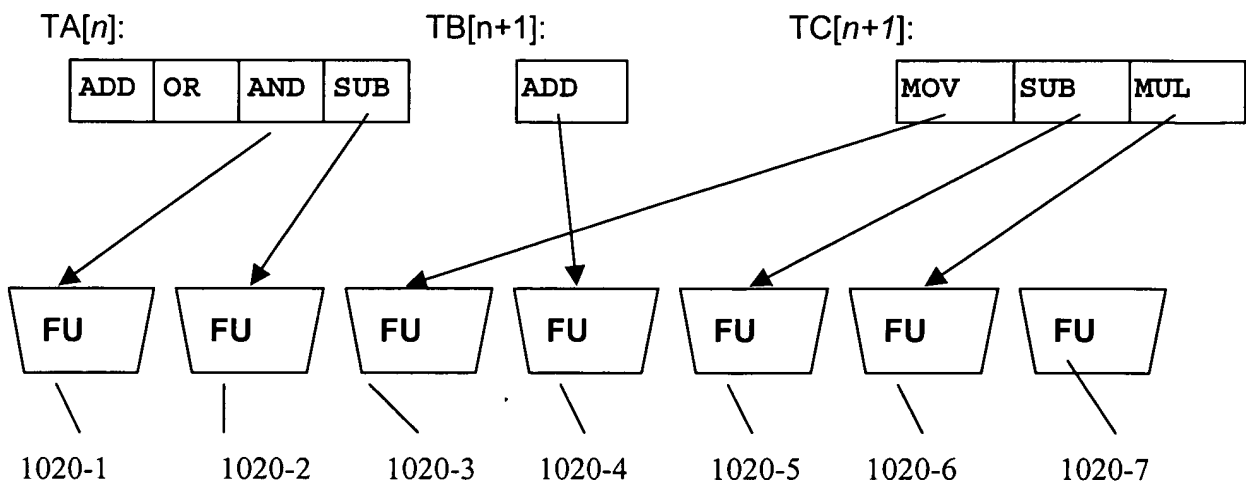


FIG. 10B